

Docket No.: W&B-INF-1947

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : ANDREAS FELBER ET AL.  
Filed : CONCURRENTLY HEREWITH  
Title : TEST STRUCTURE FOR DETERMINING A SHORT CIRCUIT  
BETWEEN TRENCH CAPACITORS IN A MEMORY CELL  
ARRAY

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent Application Publication No. 2002/0063272 A1 (Miyajima), dated May 30, 2002.

Respectfully submitted,

  
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For Applicants

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Date: September 30, 2003

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<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT          (37 CFR 1.98(b))</b>				Attorney Docket No.: W&B-INF-1947 Appl. No.:  Applicant: ANDREAS FELBER ET AL.  Filing Date: September 30, 2003 Group Art Unit:			
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EXAMINER INITIALS	A	PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	2002/0063272 A1	5/30/02	Miyajima			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT							
	J	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
<b>EXAMINER</b>	<b>DATE CONSIDERED</b>	